

Electric Circuits

Chapter 1

$$1 \text{ C} = \frac{1}{1.602 \times 10^{-19}}$$

$$e = 1.602 \times 10^{-19}$$

$$I = \frac{dq}{dt} \quad (\text{Ampere})$$

$$Q = \int_{t_0}^t I \, dt$$

Two Types of Current: -

Direct Current



remains constant with time.

Alternating current



varies sinusoidally with time.

Voltage: Is the energy required to move a unit charge through an element

$$1 \text{ volt} = 1 \text{ joule/coulomb}$$

$$V_{ab} = \frac{dw}{dq}$$

$$w = \int V \, dq$$

$$P = \frac{dw}{dt}$$

$$= \left[\frac{dw}{dq} \right] \cdot \left[\frac{dq}{dt} \right] = i \cdot V$$

$$= \frac{i^2 \cdot R}{V^2} \rightarrow \text{instantaneous power}$$

* passive sign convention
↳ The current enters through the positive Terminal

→ + -

$$P = v \cdot i > 0 \quad \text{power absorbed}$$

$$P = v \cdot i < 0 \quad \text{power supplied / delivered.}$$



power absorbed = power supplied

* قانون حفظ الطاقة *

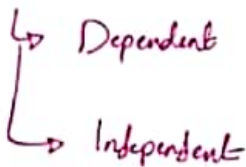
$$\sum_{i=1}^N P_i = 0$$

$$W = \int_{t_0}^t P dt = \int_{t_0}^t v \cdot i dt$$

⇒ Types of elements:-

1. active elements → Generators and Batteries / voltage or current sources
2. passive elements → Resistors, Capacitors and Inductors.

Sources



Chapter 2

Basic Laws

Ohm's Law

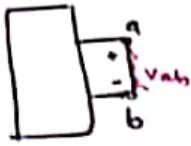
$$R = \frac{V}{I} \quad ; \quad R = \frac{V}{I} \quad \text{Volt/Ampere}$$

Conductance ; $G = \frac{1}{R} = R^{-1} \Rightarrow \Omega^{-1} \text{ S}$.

$$V = I \cdot R$$

$$V = \frac{I}{G} \Rightarrow V \cdot I$$

Short Circuits: -

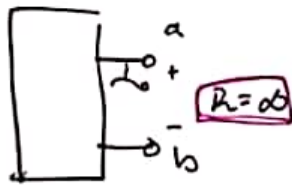


$$V_{ab} = I \cdot R = 0$$

$$V_{ab} = 0$$

$$V_a = V_b$$

Open Circuits: -



open circuit

بعض لا يوجد تيار

$$i = \frac{V}{R}$$

$$\frac{V}{\infty} = 0$$

$$\lim_{R \rightarrow \infty} \frac{V}{R} = 0$$

$$i = 0$$

Branches :- no of Branches = no. of elements

Nodes :- point of connection between 2 or more Branches

Loops: any closed path.

$$\text{branch } b = \text{ind. loop } l + n - 1$$

n nodes

KVL and KCL

Algebraic sum of currents entering a node is zero.

$$\sum_{n=1}^N i_n = 0$$

$$\sum \text{entering} = \sum \text{leaving}$$

N : # of Branches connected to the node.

KCL

Algebraic sum of all voltages around a closed path = 0

$$\sum_{m=1}^M V_m = 0$$

M : # of Branches in the loop

Voltage Divider rule / voltage division

دائرة متسلسلة
Circuit on series

$$V_1 = R_1 \cdot i = \frac{R_1 V}{R_1 + R_2}$$

$$V_2 = R_2 \cdot i = \frac{R_2 V}{R_1 + R_2}$$

$$V_i = \frac{R_i \cdot V}{R_{eq}}$$

$$i \in \{1, 2, 3, 4, \dots\}$$

Current Division / Current divider rule

دائرة متوازية
Circuits on parallel

$$i_1 = \frac{R_2}{R_1 + R_2} \cdot i \Rightarrow i_1 = \frac{G_1 \cdot i}{G_1 + G_2}$$

$$i_2 = \frac{R_1}{R_1 + R_2} \cdot i \Rightarrow i_2 = \frac{G_2 \cdot i}{G_1 + G_2}$$

$i_k =$

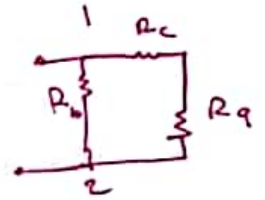
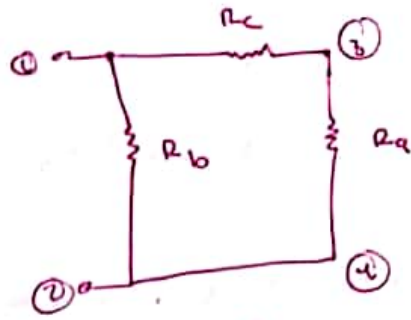
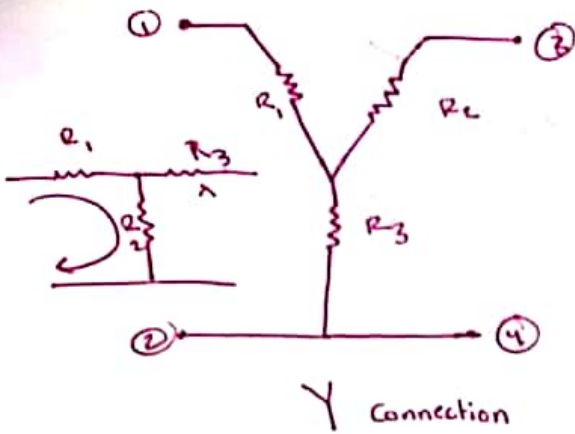
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$$i_1 = \frac{V}{R_1}$$

$$V = i_2 R_{eq}$$

$$i_1 = \frac{R_2 R_2}{R_1 + R_2}$$

Delta - Wye Connection:-



Delta Connection

Delta to wye connection

$$R_{12}(Y) = R_1 + R_3$$

$$R_{12}(\Delta) = R_b \parallel (R_a + R_c)$$

$$R_1 + R_3 = R_b \parallel (R_a + R_c) = \frac{R_b (R_a + R_c)}{R_a + R_b + R_c} \quad \dots \text{[1]}$$

$$R_1 + R_2 = R_c \parallel (R_a + R_b) = \frac{R_c (R_a + R_b)}{R_a + R_b + R_c} \quad \dots \text{[2]}$$

$$R_2 + R_3 = R_a \parallel (R_b + R_c) = \frac{R_a (R_b + R_c)}{R_a + R_b + R_c} \quad \dots \text{[3]}$$

$$R_1 = \frac{R_b \cdot R_c}{R_a + R_b + R_c}$$

$$R_2 = \frac{R_a \cdot R_c}{R_a + R_b + R_c}$$

$$R_3 = \frac{R_a + R_b}{R_a + R_b + R_c}$$

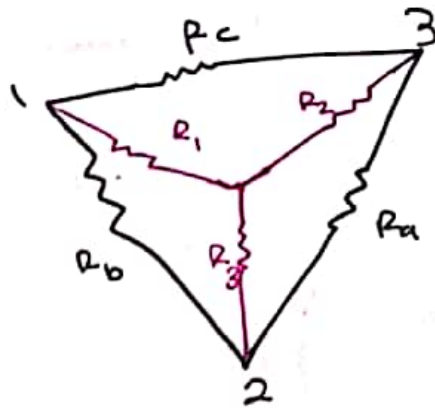
$$R = R_1 = R_2 = R_3$$

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$$R_a = \frac{R_1 R_2 + R_1 R_3 + R_2 R_3}{R_1}$$

$$R_b = \frac{R_1 R_2 + R_1 R_3 + R_2 R_3}{R_2}$$

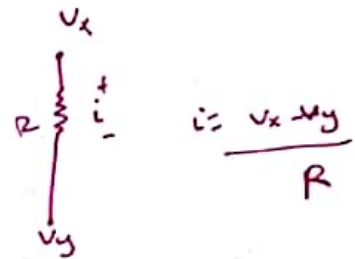
$$R_c = \frac{R_1 R_2 + R_1 R_3 + R_2 R_3}{R_3}$$



Nodal Analysis (Interested in applying KCL) Chapter Methods of Analysis.

To find Node voltages:-

- 1] select reference node
- 2] Apply KCL to nonreference node
- 3] solve equations.



— If a voltage source connected between reference node and nonreference node
voltage at nonreference node = voltage source

Voltage source (dependent/ independent) is connected between 2 nonreference node

⇒ Supernode

* Supernode can be neglected and anything is parallel to it.

Mesh Analysis :- (Interested in applying KVL)

is a loop that does not contain any other loop within it.

* The current through a mesh is known as mesh current.

To find mesh current:-

- 1] Assign mesh current.
- 2] Apply KVL.
- 3] Solve equations.

(supermesh) Mesh analysis with current sources:-

- 1] Case 1: when a current exists only in one mesh.

* \Rightarrow بس نقطہ نظر Current ہال
mesh انتسابہ و بعض عناصر ہال

- 2] Case 2:- when a current exists between two meshes.

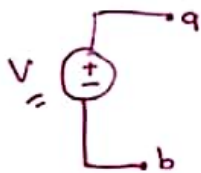
\Rightarrow we creating a supermesh by excluding the current source and any elements connected in series with it.

Superposition

Steps to apply superposition:-

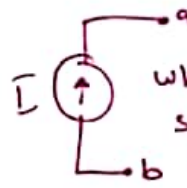
- 1] Turnoff all independent sources
- 2] Repeat step 1 for each of the other independent sources
- 3] find the total contribution

[A]



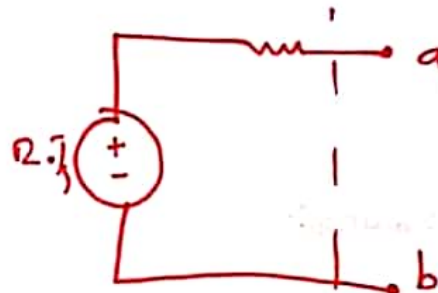
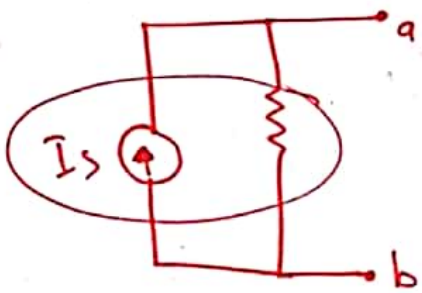
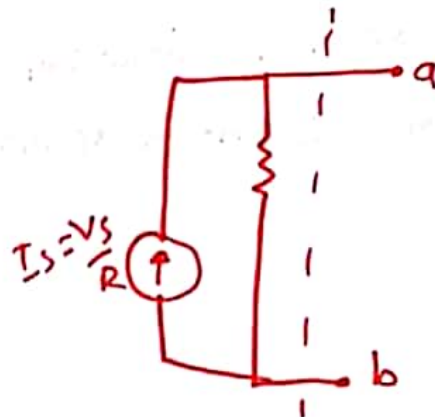
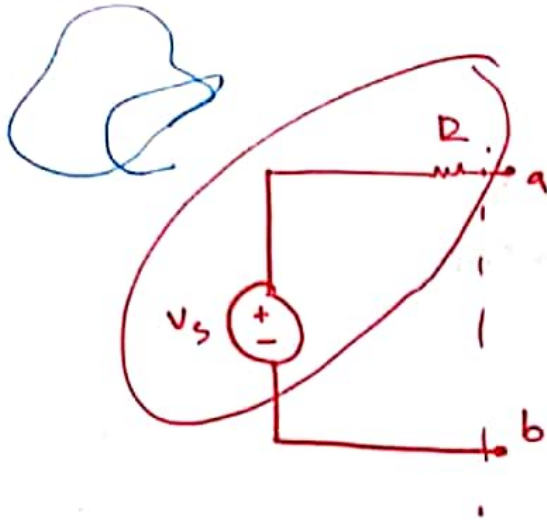
when we kill voltage source, we make it short circuit.

[B]



when we kill current source, we make it open circuit.

Source Transformation



Linear property.