Quiz #1					10	
Name: 1	ameem	Hasan	ID: 017	27430		
a) A of b) Th	processor with 1 memory. le IO.SYS progra	6-bit data bus ar	nd 24-bit addr	ess bus can ac		byte nemory, and
table belor cycle in or	w, fill in the val rder to read the i	ues that will be	on the addres	s, data, and c	ontrol lines i	n each clock
		150 <mark>0</mark> 1 4FF	5B 04 BE 00			
0 1 111	Address Bus	Data Bus	MRDC	MWTC	IORC	IOWC
Cycle #1 Cycle #2	Address Bus XXXX	Data Bus XXXX	MRDC	MWTC	IORC	TOWC
Cycle #2 Q3) a) Wr	xxxx rite the directives croprocessor: I. The number	required to define -1024 . $\rightarrow 2$ 0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00	ne the following	ng data in mer	nory on an In	
Cycle #2 Q3) a) Wr mic	xxxx rite the directives croprocessor: I. The number 0000 I. The number	required to defi -1024. -0000 -10.1875 in sing	ne the following	ng data in mer	nory on an In	tel
	Q1) Fill ir a) A of b) Th its Q2) Assurtable belocycle in o	 Q1) Fill in the blanks with a) A processor with 1 of memory. b) The IO.SYS progratis function is to Q2) Assume that an imagitable below, fill in the val 	 Q1) Fill in the blanks with the correct answall a) A processor with 16-bit data bus an of memory. b) The IO.SYS program is loaded into its function is to Q2) Assume that an imaginary processor table below, fill in the values that will be cycle in order to read the instruction locate shown in the figure below. 1503 1502 1501 1500 14FF 	Q1) Fill in the blanks with the correct answers: a) A processor with 16-bit data bus and 24-bit addressed of memory. b) The IO.SYS program is loaded into the its function is to Q2) Assume that an imaginary processor has a 16-bit datable below, fill in the values that will be on the addressed in order to read the instruction located at memory shown in the figure below. 1503 00 1502 5B 1501 04 1500 BE 14FF 00	a) A processor with 16-bit data bus and 24-bit address bus can accord memory. b) The IO.SYS program is loaded into the its function is to Q2) Assume that an imaginary processor has a 16-bit data bus and a table below, fill in the values that will be on the address, data, and c cycle in order to read the instruction located at memory location 1500 shown in the figure below. 1503 00 1502 5B 1501 04 1500 BE 1500 BE 14FF 00	Q1) Fill in the blanks with the correct answers: a) A processor with 16-bit data bus and 24-bit address bus can address up to of memory. b) The IO.SYS program is loaded into the its function is to Q2) Assume that an imaginary processor has a 16-bit data bus and a 16-bit address table below, fill in the values that will be on the address, data, and control lines it cycle in order to read the instruction located at memory location 1500H of the mem shown in the figure below. 1503 00 1502 5B 1501 04 1500 BE 14FF 000