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### Question 1

Consider the circuit shown in Fig. 1, with a current source of value  $I_1$  and  $V_I$  is voltage drop across it.

- a) Indicate the directions of  $I_1$  and the other transistors currents.

- b) Add additional components and signal(s) to the circuit for it to act as a high current amplifier.

c) Perform the following dc analysis.

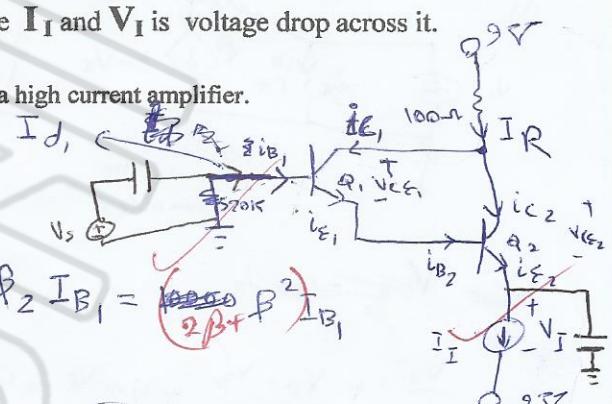
i) Express  $I_R$  in terms of  $\beta$  and  $I_{B1}$ .

$$\frac{I_0}{I_0 + I_1} = \frac{I_{B_1}}{I_{B_1} + I_{B_2}} = \frac{I_{B_2}}{I_{B_1} + I_{B_2}}$$

$$= \frac{I_1}{(1+\alpha)(1+\beta_2)}$$

ii) Write two equations involving  $V_1$ .

$$I_R = \beta_1 \beta_2 I_B = (\cancel{\beta_1 \beta_2} \beta^2) I_B$$



ii) Write two equations involving  $V_I$ .

$$0.7 + V_I - 9 + I_{BQ_1} \cdot 520 + 0.7 = 0 \rightarrow V_I + 520 I_{BQ_1} = 7.6$$

$$V_I = -V_{CEQ_2} - I_R(0.1) + 9 + 9 = 0 \quad \text{---} \quad V_I = V_{CEQ_2} - 0.1 I_R = -18$$

(iii) Solve for  $I_{B1}$  given that  $V_{CE2}=8V$ .

$$V_{I_1 + 52.0 I_{BQ_1}} = 7.6$$

$$I_{BQ_1} = \frac{I_{EQ_1}}{1+\beta_1} = \frac{I_{BQ_2}}{1+\beta_1} = \frac{I_I}{(1+\beta_1)(1+\beta_2)}$$

$$\sqrt{V_I + 52.0} I_{B_1} = 7.6$$

$$V_I = -0.1 \left( f_1 + f_2 f^2 \right) I_B = -10$$

$$I_R = I_{CQ_1} + I_{CQ_2} = \beta I_{BQ_1} + \beta I_{BQ_2}$$

$$= \beta I_{BQ_1} + \beta I_{BQ_1} (1 + \beta)$$

$$I_R = (\beta + \beta(1 + \beta)) I_{BQ_1}$$

iv) Calculate  $V_{CE1}$ .

$$V_C = -0.1 I_R + 9 + 9 - 0.7 = -0.1 (11.6758) + 17.3 \Rightarrow V_I = 1.65758 \text{ V}$$

d) Calculate  $r_{\pi}$  and  $g_m$  for the  $\mathbf{Q}_2$  transistor.

$$V_{R_2} = \frac{V_T}{I_{BQ_2}} = \frac{0.026}{I_2} = 0.02252 \quad \checkmark$$

$$g_m = \frac{I C_{\alpha_2}}{\sqrt{I}} = 4439.726 \text{ K}^{-2}$$

$$I_B = I_{\Sigma_2} = I_{B_1}(1+\beta)$$

$$= 1.154329$$

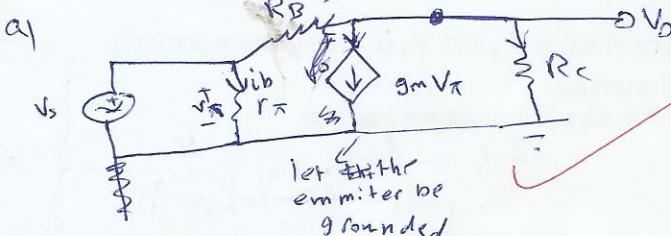
$$I_{CQ2} = f I_{B2}$$

### Question 2

Consider the circuit shown in Fig. 2

- a) Draw the small signal low frequency equivalent circuit when  $r_o = \infty \Omega$ .

- b) Use the equivalent circuit together with KCL (or otherwise) to determine the voltage gain  $A_v$  in terms of  $R_C$ ,  $R_B$  and  $g_m$ . What is  $A_v$  when  $R_B$  approaches  $\infty \Omega$ .



~~$A_v = -g_m V_T$~~

$$\frac{V_o}{R_C} + \frac{V_o - V_s}{R_B} + g_m V_T = 0 \quad \text{but } V_R = V_s$$

~~$V_o \left( \frac{1}{R_C} + \frac{1}{R_B} \right) = \left( -g_m + \frac{V_s}{R_B} \right) V_R$~~

~~$V_o = \left( -g_m + \frac{1}{R_B} \right) (R_C || R_B) V_s$~~

$$A_v = \frac{V_o}{V_s} = \left( -g_m + \frac{1}{R_B} \right) (R_C || R_B)$$

$$A_v |_{R_B \rightarrow \infty} = -g_m R_C$$

- c) Suppose  $r_o$  is now finite, can you easily write  $A_v$  in this case based on  $A_v$  already obtained in part b?

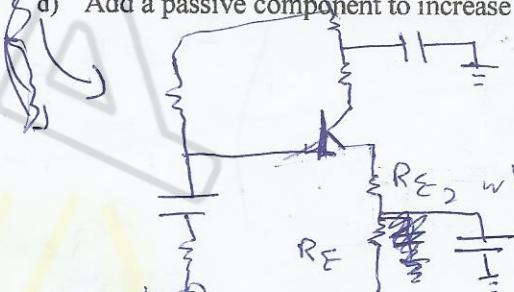
If NO, why not? If YES, what is  $A_v$  now, (Warning: Don not calculate from first principles).

$$A_v = \left[ -g_m + \frac{1}{R_B} \right] \left( (R_C || r_o) || R_B \right)$$

### Question 3

Consider the circuit shown in Fig. 3

- a) Write down the most simple expression for  $A_v = -\frac{R_C 2}{R_E}$
- b) Write down the input resistance  $R_i = R_B + (V_A + (1 + \beta) R_E)$
- c) Write down the configuration of the amplifier, and the output resistance  $R_o$ . Common emitter,  $R_o = R_E$
- d) Add a passive component to increase  $A_v$  without affecting the dc biasing.



where  $R_E 2 < R_E$  so in dc  $R_E$  will dominate

so there's no much affect due to  $R_E 2$